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Application No. 09/936,032  
Filed 03/11/2002

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*Erwin J. Basinski*  
(Erwin J. Basinski)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the application of:

Inventor: Michael NICOLAIDIS

Examiner: GANDHI, Dipakkumar B

Serial No.: 09/936,032

Filing Date: March 11, 2002

Art Unit: 2138

For: LOGIC CIRCUIT PROTECTED  
AGAINST TRANSIENT  
DISTURBANCES

**AMENDMENT UNDER 37 C.F.R. §1.121**

Mail Stop Amendments  
Commissioner for Patents  
P. O. Box 1450  
Washington, D.C. 22313-1450

Dear Sir:

This is in response to the Final Office Action dated March 1, 2007 for which a response is due by May 1, 2007, in order to receive an Advisory Action. Accordingly, this response is timely filed. Claims 1-7, 9, 10 and 12 are pending in this application; claims 1-5, 9, 10 and 12 are allowed; and claims 6 and 7 are rejected in this current Final Office Action. Applicant respectfully cancels claims 6 and 7 without prejudice and requests that the allowed and pending claims 1-7, 9, 10 and 12 be passed to issue.

Please enter the following amendments, which Applicant believes places the application and claims in condition for allowance: